

The diagram illustrates a system (1) with three main components: a SEMICONDUCTOR STORAGE MEDIUM (11), a FIRST CONTROL UNIT WITH A VERIFICATION MECHANISM (12), and a FIRST DATA INPUT / OUTPUT INTERFACE (14). The control unit (12) is connected to the storage medium (11) via an **ENABLED** signal line. Additionally, the control unit (12) is connected to the data interface (14) through a bus structure (13), which also provides a bidirectional connection between the storage medium (11) and the interface (14).

FIG. 1

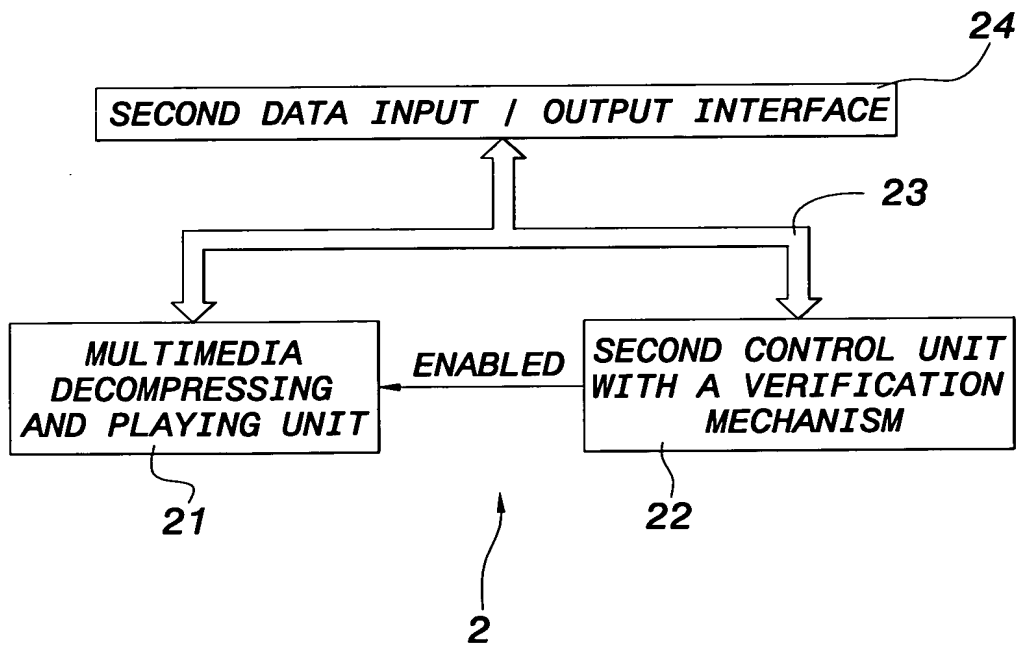


FIG. 2

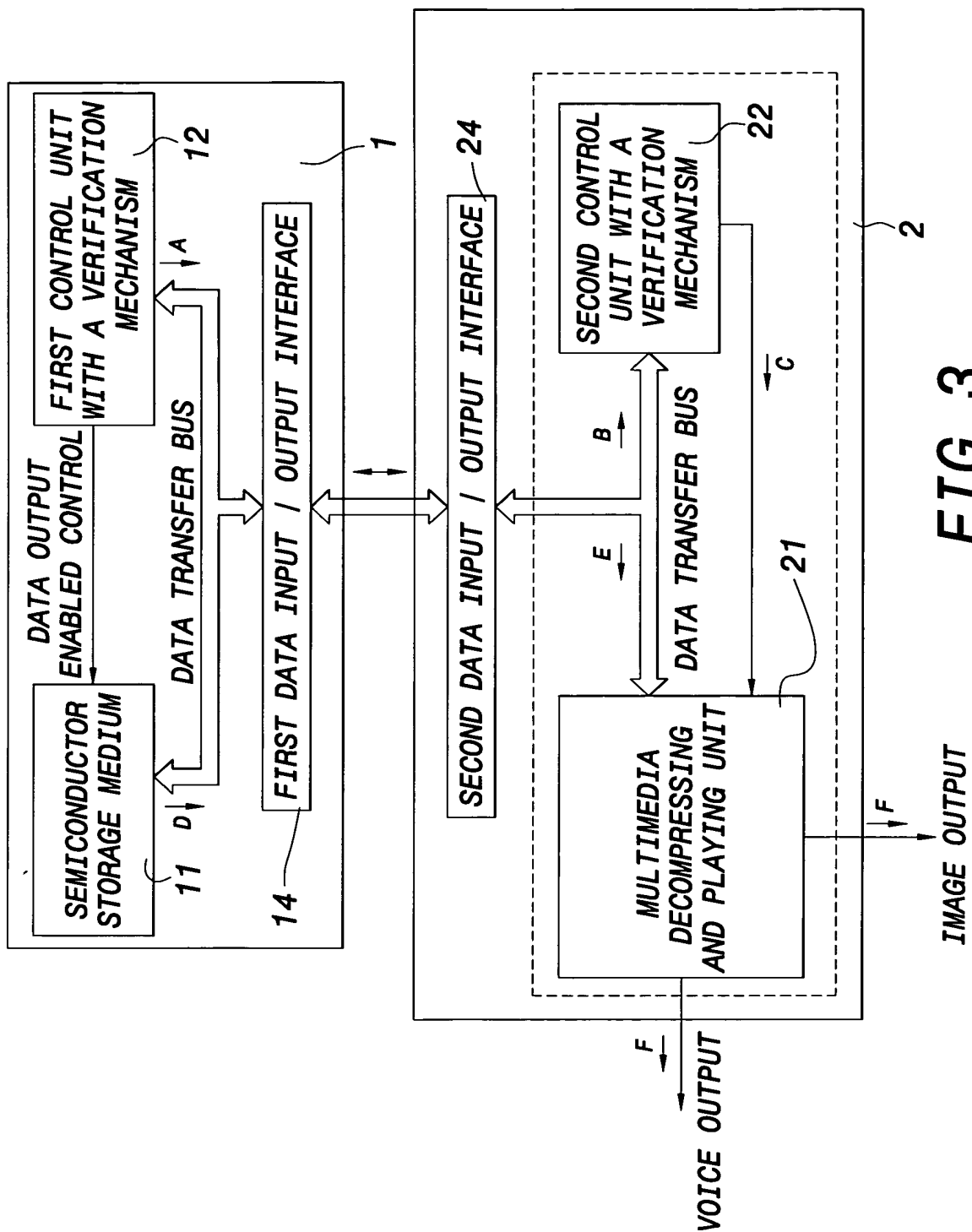


FIG. 3

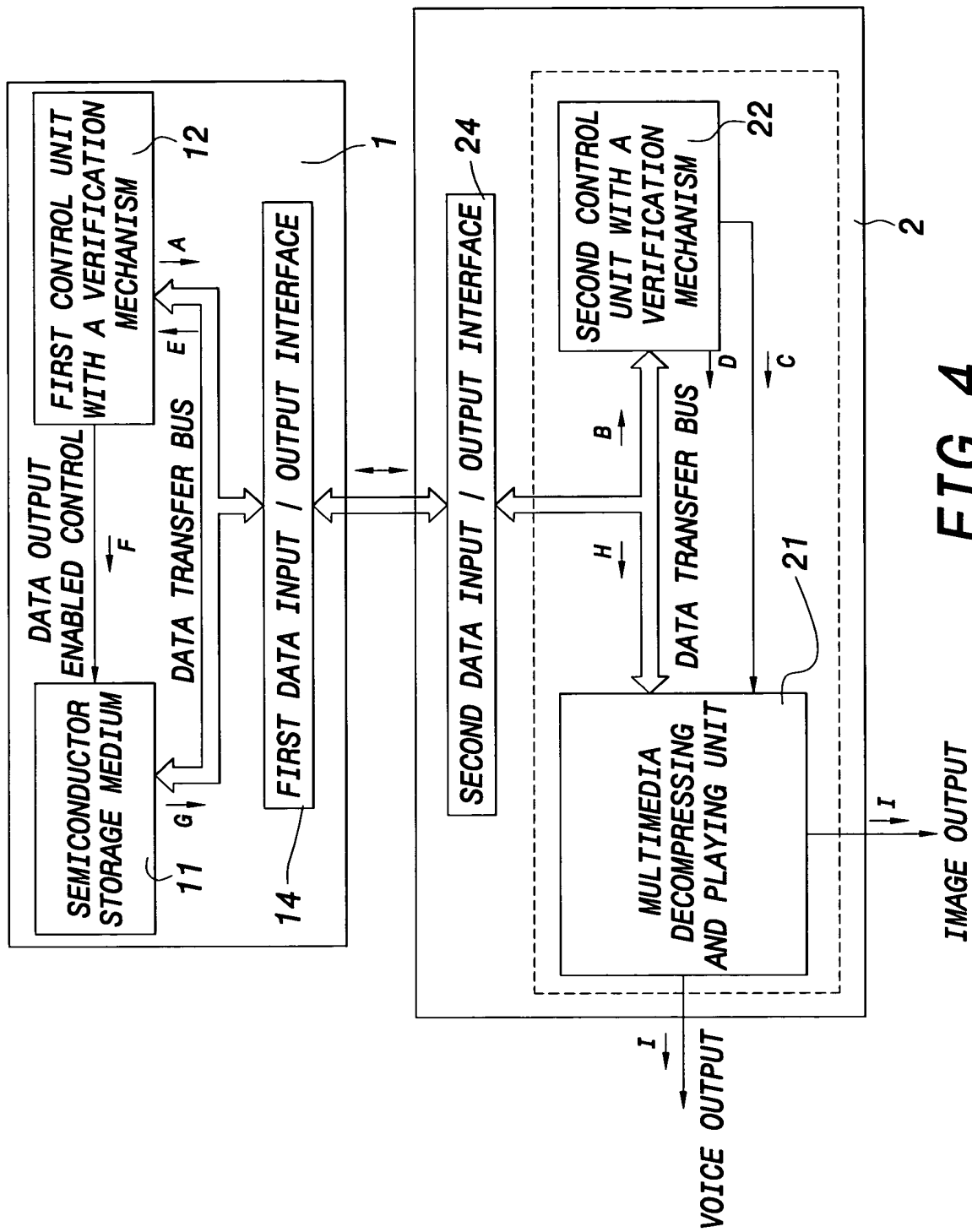


FIG. 4